

Refine Search

Search Results -

Term	Documents
(7 AND 23).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	13
(L23 AND L7).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	13

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L23 and 18

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Monday, March 12, 2007 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L27</u>	L23 and 17	13	<u>L27</u>
<u>L26</u>	L23 and 16	160	<u>L26</u>
<u>L25</u>	L23 and 15	118	<u>L25</u>
<u>L24</u>	L23 and 14	389	<u>L24</u>
<u>L23</u>	L16 and (scan\$5 or test\$5 or evaluat\$5)	5410	<u>L23</u>
<u>L22</u>	L17 and 18	251	<u>L22</u>
<u>L21</u>	L17 and 17	12	<u>L21</u>
<u>L20</u>	L17 and 16	160	<u>L20</u>
<u>L19</u>	L17 and 15	111	<u>L19</u>
<u>L18</u>	L17 and 14	366	<u>L18</u>
<u>L17</u>	L16 and (test\$5 or evaluat\$5)	4969	<u>L17</u>

<u>L16</u>	boolean near10 (assembl\$4 or microinstruction\$1 or instruction\$1 or command\$1 or expression\$1 or statement\$1)	6797	<u>L16</u>
<u>L15</u>	l9 and l6	4	<u>L15</u>
<u>L14</u>	l9 and l5	28	<u>L14</u>
<u>L13</u>	l9 and l4	110	<u>L13</u>
<u>L12</u>	l2 and l6	4	<u>L12</u>
<u>L11</u>	l2 and l5	29	<u>L11</u>
<u>L10</u>	l2 and l4	112	<u>L10</u>
<u>L9</u>	L3 and (assembl\$4 or microinstruction\$1 or instruction\$1 or command\$1 or expression\$1 or statement\$1)	283	<u>L9</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L8</u>	(717/124-161)![CCLS]	6849	<u>L8</u>
<u>L7</u>	(326/41,114,125)[CCLS]	1745	<u>L7</u>
<u>L6</u>	(716/15-19)[CCLS]	3770	<u>L6</u>
<u>L5</u>	(712/220-227)![CCLS]	2820	<u>L5</u>
<u>L4</u>	(712/2-300)[CCLS]	13116	<u>L4</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L3</u>	L2 and (test\$5 or evaluat\$5)	284	<u>L3</u>
<u>L2</u>	L1 and address\$5 near4 register\$1 near25 (select\$7 or mux or multiplex\$6)	302	<u>L2</u>
<u>L1</u>	(compil\$6 or debug\$5 or logic) near18 (boolean) and (conjunct\$4 or disjunct\$5) and ("OR" or "AND")	2780	<u>L1</u>

END OF SEARCH HISTORY